

ABSTRACT

A phase-locked loop including an oscillator, controlled by a control signal generated by a comparison circuit comparing a reference frequency and the oscillator frequency and filtered by an integrator low-pass filter; a control and adjustment circuit for, with a predetermined frequency smaller than the reference frequency, taking into account the value of the filtered controlled signal and, if this value is out of a range of predetermined values, adjusting the operating range of the oscillator; and an inhibition circuit for deactivating the comparison circuit for a predetermined duration before taking into account the value of the filtered control signal.